

Notice of References Cited

Application/Control No.

10/021,195

Applicant(s)/Patent Under
Reexamination
COLLINS ET AL.

Examiner

Luz L. Alejandro

Art Unit

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Page 1 of 1

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*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
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	M	US-			

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NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Wolf et al., "Silicon Processing for the VLSI Era Volume 1: Process Technology", pages 568-572, Lattice Press, 1986.
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*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
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